





DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IQAC X Internal Quality Assurance Cell



**IOAC Initiative** 

## "3-Days Workshop on Recent trends in Design of Digital

System using EDA Tools"

15/06/23 to 17/06/23

**Resource Person/s** 

Dr. Srinivasarao Udara Trainer, JVT Hub, Ranebennur

Repika.V.B

Neering **Event** coordinators

Is. Deepika V B / Mrs. Veena H T Assistant Professor

Checked by Dr. Sunil Kumar B S Dean Academics

DEAN ACADEMICS G.M. Institute Of Technology DAVANGERE-577006.

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Verified Bv Dr. Praveen J Head of the Department DAVANGERE-577 006

Checked by

Dr. Praveen J **IOAC-Director** GM Institute of Technology Davangere - 577 006.

Approved by Dr. Sanjay Pande M B Principal PRINCIPAL GM Institute of Technology Davangere - 577 006.



Srishyla Educational Trust (R), Bheemasamudra GM INSTITUTE OF TECHNOLOGY, DAVANGERE Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka



## Department of Electronics and Communication Engineering

Invitation

We cordially invite you for the

**IQAC Initiative** 

"3-Days Workshop on Recent trends in Design of Digital

#### System using EDA Tools"

in association with BJV Tech Hub.

Cenuine Way To Success...

IQAC Internal Quality Assurance Cell

15<sup>th</sup> to 17<sup>th</sup> June 2023

Convener: Dr. Praveen J IQAC-Director Professor & Head Dept. of ECE, GMIT President: Dr. Sanjay Pande M B Principal GMIT, Davangere

## **Resource Person**

**Dr. Srinivasarao Udara** Trainer **Ranebennur** 

#### **Coordinators**

Ms. Deepika V B Assistant Professor Dept. of ECE, GMIT Mrs. Veena H T Assistant Professor Dept. of ECE, GMIT

## **Organized By:**

Department of Electronics and Communication Engineering Venue: VLSI Lab, ECE Department.

Date: 15th June 2023 Time: 9:30 AM



## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

## Report On "3-Days workshop on Recent trends in Design of Digital System using EDA Tools"

The Department of Electronics and Communication Engineering, GM Institute of Technology, Davanagere organized a Technical Workshop on "3-Days workshop on **Recent trends in Design of Digital System using EDA Tools**" by the resource person **Dr. Srinivasarao Udara**, BJV tech hub, Ranebennur from 15<sup>th</sup> to 17<sup>th</sup> June 2023 at 9.30 am to 5.00 pm in VLSI Lab.

The workshop was inaugurated on 15<sup>th</sup> June 2023 at 9:00AM. The resource persons, **Dr. Srinivasarao Udara**, BJV tech hub, Ranebennur **Dr. Sanjay Pande M B**, Principal, GMIT, **Dr. Praveen** J, HOD, ECE Department, the Event Coordinators **Ms. Deepika V B** and **Mrs. Veena H T** faculties and registered students of workshop were present in the function.

The Principal and HOD addressed all the students and informed to benefit from the workshop and use in real time applications.

**Dr. Praveen J**, Professor & Head, ECE department, was presided over the Workshop and welcomed the gathering and also briefed the assembled students about the importance of VLSI in present technologies. The faculty members of ECE and the students of ECE were present in the Workshop.

The key note speaker of the Workshop **Dr. Srinivasarao Udara**, started the presentation by giving introduction about the VLSI, usage and scope of Cadence tool in Academics as well as in Industry. He also gave technical importance of Cadence tool, he also conducted Hands on Session for designing and simulating digital circuit using Verilog HDL, Synthesis process of Digital circuits, Evaluation of synthesis reports to obtain optimum gate level Net List, design and simulate basic CMOS circuit and performed RTL-GDSII flow.

The resource persons encouraged all the students by giving awareness about importance of Cadence Virtuoso Platform in VLSI research field in both Academics and Industries to build their Career. The students were motivated and benefited.



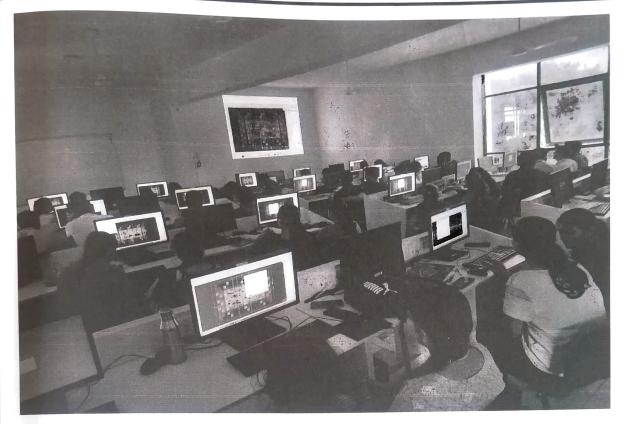


Figure 1: Hands -on Session on Cadence Tool



Figure 2: Theory session by the resource person



# DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



Figure 3: Workshop Attended Batch-1 students team



Figure 4: Felicitation to the Resource person



IQAC



GM INSTITUTE OF TECHNOLOGY

P. B. Road, Davanagere - 577 006, Karnataka, INDIA

IQAC Initiative Department of Electronics & Communication Engineering In association with BJV Tech Hub.



This is to Certify that Mr. 1Ms.

Participated in a 3-Days workshop on "**Recent Trends in Design of Digital System using EDA Tools" from** 15" to 17" of June 2023 organized by Department of Communication Engineering, GM Institute of Technology, Davangere.

Dr. Srinivasarao Udara Trainer, BJV, Tech Hub. Dr. Praveen J Convenor Dr. Sanjay Pande M B Principal has